

Details Information of Faculty Members:

1.	Name	KONIKA DAS(BHATTACHARYA)
2.	Designation	PROFESSOR
3.	Contact Details(e-mail id Inst. id , alternative e-mail, ph. With extn., mobile(if any))	Inst. Email: kdb@ee.iiests.ac.in
		Alter. Email:poopolee50@hotmail.com
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4.	Qualification	B.E (Electrical Engineering) B.E College, Cal.Univ. M.E (Power Systems) B.E College, Cal.Univ Ph.D (Engg.)- B.E College, D.U
5.	About Yourself	Research: <ol style="list-style-type: none"> 1. A Performance Measurement Method for I-V tracing of Solar PV Generators, Date of Filing 3rd Nov, 2018,Patent Filing No-201831041698 2. Development of a Micro Grid at IESTS, Ministry of Power, Govt.of West Bengal, 2014-2016 3. Post-Doctoral: SUBSTATION AUTOMATION AND COMMUNICATION IN A MODERN POWER SYSTEM LABORATORY, at Victoria Univ., Melbourne, Australia under Endeavor Awards, Government of Australia, 2011 4. A Smart energy Saving Device for Power Factor Control And Power Quality Monitoring, Amt. Rs.17.0 Lacs, CSIR, New Delhi, 2010
		Teaching: Both UG and PG Since 1998 till date
		Industrial Experience: <ul style="list-style-type: none"> • As Management Trainee in the Bhilai Steel Plant for one year • Developed Power Saving Device for Local MSME Units for Reduction of the Electricity Billing
6.	Photograph	Please send recent photo to the HOD e-mail id 
7.	Research Areas	<ul style="list-style-type: none"> • Application of Embedded System in Power System Protection, Automation and Control. • Micro Grid Studies for Rural and Urban Areas • Development of the Intelligent Micro Grid Controller • Smart Grid Initiatives • Photo Voltaic Energy Measurements
8.	Course Taught	UG <ul style="list-style-type: none"> • Microprocessors • Digital Signal Processing (DSP) • Electric Traction • Illumination Engineering
		PG <ul style="list-style-type: none"> • Computer Applications for Power System Protection • Illumination Science, Engineering and Design • Advanced Microcontroller Technology